

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	7	((base underl\$5 undercoat\$4) near2 (non\$1uniform thick\$4))and (lcd electroluminescent display\$2 soi silicon\$1on\$1insulator\$2 silicon adj2 insulator\$2 tft thin adj film adj transistor\$2).ti. and (gate)adj (insulat\$4 dielectric oxide)	EPO; JPO; DERWENT	OR	OFF	2006/08/19 01:48
L2	8	((base underl\$5 undercoat\$4) near2 (non\$1uniform thick\$4))and (lcd electroluminescent display\$2 soi silicon\$1on\$1insulator\$2 silicon adj2 insulator\$2 tft thin adj film adj transistor\$2) and (gate)adj (insulat\$4 dielectric oxide)	EPO; JPO; DERWENT	OR	OFF	2006/08/19 01:51
L3	1	2 not 1	EPO; JPO; DERWENT	OR	OFF	2006/08/19 01:48
L4	36	((base underl\$5 undercoat\$4)with (non\$1uniform thick\$4))and (lcd electroluminescent display\$2 soi silicon\$1on\$1insulator\$2 silicon adj2 insulator\$2 tft thin adj film adj transistor\$2) and (gate)adj (insulat\$4 dielectric oxide)	EPO; JPO; DERWENT	OR	OFF	2006/08/19 02:04
L5	28	4 not 2	EPO; JPO; DERWENT	OR	OFF	2006/08/19 01:51
L6	4875	((base underl\$5 undercoat\$4)with (non\$1uniform thick\$4))and (lcd electroluminescent display\$2 soi silicon\$1on\$1insulator\$2 silicon adj2 insulator\$2 tft thin adj film adj transistor\$2) and (gate)adj (insulat\$4 dielectric oxide)	US-PGPUB; USPAT	OR	OFF	2006/08/19 02:01
L7	1276	((base underl\$5 undercoat\$4)with (non\$1uniform thick\$4))and (lcd electroluminescent display\$2 soi silicon\$1on\$1insulator\$2 silicon adj2 insulator\$2 tft thin adj film adj transistor\$2).ti. and (gate)adj (insulat\$4 dielectric oxide)	US-PGPUB; USPAT	OR	OFF	2006/08/19 02:01

EAST Search History

L8	100	((base underl\$5 undercoat\$4)with (non\$1uniform thick\$4)).clm,ab. and (lcd electroluminescent display\$2 soi silicon\$1on\$1insulator\$2 silicon adj2 insulator\$2 tft thin adj film adj transistor\$2).ti. and (gate)adj (insulat\$4 dielectric oxide)	US-PGPUB; USPAT	OR	OFF	2006/08/19 02:11
L9	114	((underl\$5 undercoat\$4)with (non\$1uniform thick\$4))and (lcd electroluminescent display\$2 soi silicon\$1on\$1insulator\$2 silicon adj2 insulator\$2 tft thin adj film adj transistor\$2)	EPO; JPO; DERWENT	OR	OFF	2006/08/19 02:05
L10	103	9 not 4	EPO; JPO; DERWENT	OR	OFF	2006/08/19 02:05
L11	0	((base underl\$5 undercoat\$4)with (non\$1uniform thick\$4)) and (lcd electroluminescent display\$2 soi silicon\$1on\$1insulator\$2 silicon adj2 insulator\$2 tft thin adj film adj transistor\$2)and top adj gate	EPO; JPO; DERWENT	OR	OFF	2006/08/19 02:12
L12	0	((base underl\$5 undercoat\$4)with (non\$1uniform thick\$4)) and (lcd electroluminescent display\$2 soi silicon\$1on\$1insulator\$2 silicon adj2 insulator\$2 tft thin adj film adj transistor\$2)and staggered	EPO; JPO; DERWENT	OR	OFF	2006/08/19 02:12
L13	986	((base underl\$5 undercoat\$4)with (non\$1uniform thick\$4)) and (lcd electroluminescent display\$2 soi silicon\$1on\$1insulator\$2 silicon adj2 insulator\$2 tft thin adj film adj transistor\$2)and top adj gate	US-PGPUB; USPAT	OR	OFF	2006/08/19 02:12
L14	552	((base underl\$5 undercoat\$4)with (non\$1uniform thick\$4)) and (lcd electroluminescent display\$2 soi silicon\$1on\$1insulator\$2 silicon adj2 insulator\$2 tft thin adj film adj transistor\$2)and top adj gate	US-PGPUB	OR	OFF	2006/08/19 02:18
L16	229	((base underl\$5 undercoat\$4)with (projection)) and (lcd electroluminescent display\$2 soi silicon\$1on\$1insulator\$2 silicon adj2 insulator\$2 tft thin adj film adj transistor\$2).ti.	US-PGPUB	OR	OFF	2006/08/19 02:22

EAST Search History

L17	354	((base underl\$5 undercoat\$4)with (protru\$5)) and (lcd electroluminescent display\$2 soi silicon\$1on\$1insulator\$2 silicon adj2 insulator\$2 tft thin adj film adj transistor\$2).ti.	US-PGPUB	OR	OFF	2006/08/19 02:22
L18	278	((base underl\$5 undercoat\$4)near7 (protru\$5)) and (lcd electroluminescent display\$2 soi silicon\$1on\$1insulator\$2 silicon adj2 insulator\$2 tft thin adj film adj transistor\$2).ti.	US-PGPUB	OR	OFF	2006/08/19 02:24
L19	579	((base underl\$5 undercoat\$4)near7 (protru\$5)) and (lcd electroluminescent display\$2 soi silicon\$1on\$1insulator\$2 silicon adj2 insulator\$2 tft thin adj film adj transistor\$2)	EPO; JPO; DERWENT	OR	OFF	2006/08/19 02:24
L20	403	((base underl\$5 undercoat\$4)near7 (protru\$5)) and (lcd electroluminescent display\$2 soi silicon\$1on\$1insulator\$2 silicon adj2 insulator\$2 tft thin adj film adj transistor\$2)	DERWENT	OR	OFF	2006/08/19 02:24
L21	176	((base underl\$5 undercoat\$4)near7 (protru\$5)) and (lcd electroluminescent display\$2 soi silicon\$1on\$1insulator\$2 silicon adj2 insulator\$2 tft thin adj film adj transistor\$2)	EPO; JPO	OR	OFF	2006/08/19 02:25
L22	77	((base underl\$5 undercoat\$4)near7 (protru\$5)) and (lcd electroluminescent display\$2 soi silicon\$1on\$1insulator\$2 silicon adj2 insulator\$2 tft thin adj film adj transistor\$2).ti.	EPO; JPO	OR	OFF	2006/08/19 02:25
L23	0	((base underl\$5 undercoat\$4)near7 (protru\$5)) and (lcd electroluminescent display\$2 soi silicon\$1on\$1insulator\$2 silicon adj2 insulator\$2 tft thin adj film adj transistor\$2).ti. and (source and drain and gate)	EPO; JPO	OR	OFF	2006/08/19 02:26

EAST Search History

L24	0	((base underl\$5 undercoat\$4)near7 (protru\$5)) and (lcd electroluminescent display\$2 soi silicon\$1on\$1insulator\$2 silicon adj2 insulator\$2 tft thin adj film adj transistor\$2).ti. and (source and drain and gate)	DERWENT	OR	OFF	2006/08/19 02:27
L25	0	((base underl\$5 undercoat\$4)near7 (protru\$5)) and (lcd electroluminescent display\$2 soi silicon\$1on\$1insulator\$2 silicon adj2 insulator\$2 tft thin adj film adj transistor\$2) and (source and drain and gate)	DERWENT	OR	OFF	2006/08/19 02:26
L26	0	((base underl\$5 undercoat\$4)near7 (protru\$5)) and (lcd electroluminescent display\$2 soi silicon\$1on\$1insulator\$2 silicon adj2 insulator\$2 tft thin adj film adj transistor\$2) and (drain and gate)	DERWENT	OR	OFF	2006/08/19 02:26
L27	1	((base underl\$5 undercoat\$4)with(protru\$5 prjection)) and (lcd electroluminescent display\$2 soi silicon\$1on\$1insulator\$2 silicon adj2 insulator\$2 tft thin adj film adj transistor\$2) and (drain and gate)	DERWENT	OR	OFF	2006/08/19 02:27
L28	84	((base underl\$5 undercoat\$4)near7 (protru\$5)) and (lcd electroluminescent display\$2 soi silicon\$1on\$1insulator\$2 silicon adj2 insulator\$2 tft thin adj film adj transistor\$2).ti. and (source and drain and gate)	US-PGPUB; USPAT	OR	OFF	2006/08/19 02:27